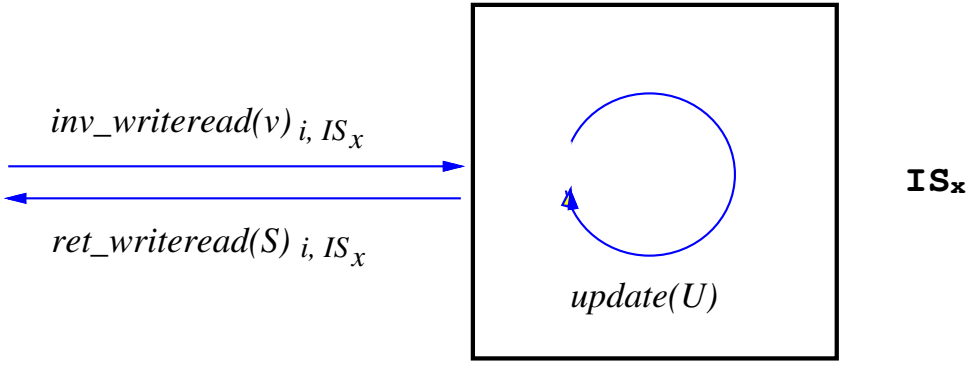
inv_writeread(v) i, IS x
ret_writeread(S) i, IS x
update(U)
ISx