

Massachusetts Institute of Technology  
Department of Electrical Engineering and Computer Science

Proposal for Thesis Research in Partial Fulfillment  
of the Requirements for the Degree of  
Master of Science

Title: A Novel Design of a Delay-Line Pulse Integrator

Submitted by:

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(Signature of author)

Date of submission: March 10, 1992

Expected Date of Completion: September 1992

Laboratory where thesis will be done: Research Laboratory of Electronics

Brief Statement of the Problem:

Recirculating quartz delay lines have frequently been employed for pulse integration in radar systems. A novel modulation and feedback system is proposed which should permit much longer effective integration time to be achieved with the same stability margin as in present designs. A bread-board model will be constructed.

Supervision Agreement:

The program outlined in this proposal is adequate for a Master's thesis. The supplies and facilities required are available, and I am willing to supervise the research and evaluate the thesis report.

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D. E. Fox, Assoc. Prof. of Elec. Eng.

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G. H. Iverson, Company Supervisor