

Massachusetts Institute of Technology
Department of Electrical Engineering and Computer Science

Proposal for Thesis Research in Partial Fulfillment
of the Requirements for the Degree of
Doctor of Philosophy

Title: A Novel Design of a Delay-Line Pulse Integrator

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Laboratory where thesis will be done: Research Laboratory of Electronics

Brief Statement of the Problem:

Recirculating quartz delay lines have frequently been employed for pulse integration in radar systems. A novel modulation and feedback system is proposed which should permit much longer effective integration time to be achieved with the same stability margin as in present designs. A bread-board model will be constructed.