Efficient Detection of Determinacy Race in Transactional Cilk Programs

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Outline

- Definition determinacy race in transactional Cilk
- ► Algorithm *T. E. R. D*.
- ► Implementation *Cilk runtime system & cilk2c*
- Performance *Time: O(Ta(v, v)), Space: O(v)*

Empirical: 15 times slowdown vs. serial execution

- Conclusion & Future Work
- Performance of Transactional Cilk

Impossibility of achieving linear speedup

Definition of Determinacy Race





- ► Efficiency i Size of transaction
- Only if correctness is not affected



- ► Atomic-thread atomization
- ► Detection: NP-complete



List Insertion (read & write "head")

Definition of Determinacy Race



read-read-1[1],	read-read-2[1],
read-write-1[l],	<pre>read-write-2[1],</pre>
writ-read-l[l],	write-read-2[1],
write-write-1[1],	write-write-2[1],
last-read[l],	last-parallel-read[1],
last-write[l],	<pre>last-parallel-write[1],</pre>
trans-id-read[1],	<pre>trans-id-write[1]</pre>

Record access: 14 shadow spaces

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- ► *Extension* of *SP-bags* algorithm
- Disjoint-Set data structure

Read memory location 1 by Transaction T Procedure F: If (trans-id-read[1]!=T && trans-id-write[1]!=T) $trans-id-read[1] \leftarrow T$ Eval-Read (1, T, F)

Eval-Read (1, T, F)

// check and report determinacy race
// update record (shadow spaces)

write memory location 1 by Transaction T Procedure F:
 If (trans-id-write[1]!=T)

 $trans-id-read[1] \leftarrow T$

 $trans-id-write[1] \leftarrow T$

Eval-Write (1, T, F)

Eval-Write (l,T,F)

// check and report determinacy race
// update record (shadow spaces)

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T : serial execution time

v : number of shared locations being monitored

 α : inverse of Ackermann's function

Time: $O(T \alpha(v, v))$

Space: *O(v)*

Transactional Nondeterminator

Implemented T.E.R.D. in Cilk runtime system

Cracked Cilk compiler "cilk2c"

Tested 15 times slowdown vs. serial execution

Programs	Serial (no T.D)	Serial (with T.D.)	Slowdown
Fib (30)	3.1 sec	9.6 sec	3.21
C.K. (5, 8)	2.2 sec	31.2 sec	14.18
L.U. (512x512)	1.1 sec	10.6 sec	9.63

Transactional Cilk Performance

 T_1 : total work for serial execution, parallel execution ??? T_{∞} : critical path length, parallel execution ??? Best case: *no abort/retry, or abort/retry does not affect* T_P Worst case: T_1 (*no parallelism, although many spawns*)

 $\frac{T_{I}/P >> T_{\infty}}{Randomized Work-Stealing} \rightarrow Linear Speedup ???$



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There exists a transactional Cilk program with T_1 as the serial execution time and T_{∞} as the minimum time required by the execution of infinite number of processor, where T_{∞} is $O(p^{1/2})$, and $T_1/p >> T_{\infty}$ the execution time on p processor is greater or equal to $p^{1/2} (T_1/p) - not$ linear speedup

- *p* is total number of processors
- X_n is the number of working processors
- Y_n is the number of trapped processors
- $n \text{ is from 1 to } T_{\infty} , X_{I} = 1, Y_{I} = 0$ $X_{n} = \begin{cases} X_{n} + 1 & 1 ((p-2)/(p-1))^{p-Xn-Yn} \\ X_{n} & \text{otherwise} \end{cases}$ $Y_{n} = \begin{cases} Y_{n} & ((p-X_{n})/(p-1))^{p-Xn-Yn} \\ Y_{n} + 1 & \text{otherwise} \end{cases}$

 $E[X_n] = -2n/p + n/8 + n^2/16p + n^2/4p^2$ $n = T_{\infty} = p^{1/2}$ $E[X_n] = O(p^{1/2})$ Note that, E[Xn] always increasing

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Conclusion & Future Work

Determinacy race definition: *Semantics* **?**

Algorithm and data-structure for maintaining relationship between transactions: *linear time*

More Language features: *inlet, wildcard, etc*

Performance of transactional Cilk: ⁽²⁾

Backup Slides

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TERD algorithm & proof, lemma

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```
Cilk char *nqueens(char *board, int n, int row)
              char *new_board;
      {
               . . .
              new_board = malloc(row+1);
              memcpy(new_board, board, row);
              for (j=0; j<n; j++) {</pre>
                   . . .
                  new_board[row] = j;
                  spawn nqueens(new_board, n, row+1);
                  . . .
             }
             sync;
      }
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```

No blocking case

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blocking case

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summary

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